

19. (new) A computer system according to claim 10, wherein some of the peripheral devices which transmit data at a higher rate may switch to a higher priority level at higher levels of buffer occupancy than others.

20. (new) A computer system according to claim 10, wherein some of the peripheral devices which have a smaller buffer for transmitting data may switch to a higher priority level at higher levels of buffer occupancy than others.

REMARKS

By this preliminary amendment, applicants respectfully request that the claims as filed be amended as described above. The claim amendments have been made to remove multiple dependencies from the claims and further clarify the claimed invention. None of the amendments have been made to overcome prior art.

Date

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Respectfully submitted,

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